Please find below and/or attached an Office communication concerning this application or proceeding.

-- *The MAILING DATE of this communication appears on the cover sheet with the correspondence address* --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) ☒ Responsive to communication(s) filed on *14 December 2001*.
2a) ☐ This action is **FINAL**.   2b) ☒ This action is non-final.
3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) ☒ Claim(s) *1-25* is/are pending in the application.
    4a) Of the above claim(s) _____ is/are withdrawn from consideration.
5) ☐ Claim(s) _____ is/are allowed.
6) ☒ Claim(s) *1-25* is/are rejected.
7) ☐ Claim(s) _____ is/are objected to.
8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) ☒ The specification is objected to by the Examiner.
10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
    Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
    Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
    a) ☐ All   b) ☐ Some * c) ☐ None of:
      1. ☐ Certified copies of the priority documents have been received.
      2. ☐ Certified copies of the priority documents have been received in Application No. _____.
      3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
    * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date _____.
5) ☐ Notice of Informal Patent Application (PTO-152)
6) ☐ Other: _____.

DETAILED ACTION

Specification

1.   The disclosure is objected to because of the following informalities: Examiner

respectfully requests Applicant to replace attorney docket numbers present in Cross-

Reference to Related Applications section of disclosure with serial numbers of the

related cases.

Appropriate correction is required.

Double Patenting

2.   The nonstatutory double patenting rejection is based on a judicially created
doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the
unjustified or improper timewise extension of the "right to exclude" granted by a patent
and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11
F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225
USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA
1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); and *In re Thorington*,
418 F.2d 528, 163 USPQ 644 (CCPA 1969).
A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be
used to overcome an actual or provisional rejection based on a nonstatutory double
patenting ground provided the conflicting application or patent is shown to be commonly
owned with this application. See 37 CFR 1.130(b).
Effective January 1, 1994, a registered attorney or agent of record may sign a
terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with
37 CFR 3.73(b).

Claims 1, 10, 11, 18, 19,25 are provisionally rejected under the judicially created

doctrine of obviousness-type double patenting as being unpatentable over claims

1,8,9,11 of copending Application No. 10/017174.

Although the conflicting claims are not identical, they are not patentably distinct from each other because claims 1, 8, 9, 11 of application 10/017174 essentially teach the same steps/means as claims 1, 11, 19 of current application.

Even though claim 1 of current application is broadened by omitting certain limitations such as input stage transmits bids to switching stage/crossbar device to request connections through switching stage/crossbar device for routing of data to output stage, a crossbar device comprising a bid arbitrator configured to determine whether to accept or reject each received bid and a memory for storing one or more accepted cells for same output device, it has been held that the omission of an element and its function is an obvious expedient if the remaining elements perform the same function as before. In re Karlson, 136 USPQ 184(CCPA). Also note Ex parte Rainu, 168 USPQ 375 (Bd. App. 1969); omission of a reference element whose function is not needed would be an obvious variation.

Even though claims 11,19 of current application are broadened by omitting certain limitations such as crossbar device comprising a bid arbitrator configured to determine whether to accept or reject each received bid and a memory for storing one or more accepted cells for same output device, it has been held that the omission of an element and its function is an obvious expedient if the remaining elements perform the same function as before. In re Karlson, 136 USPQ 184(CCPA). Also note Ex parte Rainu, 168 USPQ 375 (Bd. App. 1969); omission of a reference element whose function is not needed would be an obvious variation.

Claims 10, 18, 25 of current application are rejected based on the same rationale

given for the rejection of independent claims 1, 11, and 19 of current application.

3.   This is a <u>provisional</u> obviousness-type double patenting rejection because the

conflicting claims have not in fact been patented.

Claim Rejections - 35 USC § 112

4.   The following is a quotation of the first paragraph of 35 U.S.C. 112:

> The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

5.   Claims 15, 20 are rejected under 35 U.S.C. 112, first paragraph, as failing to

comply with the written description requirement. The claim(s) contains subject matter

which was not described in the specification in such a way as to reasonably convey to

one skilled in the relevant art that the inventor(s), at the time the application was filed,

had possession of the claimed invention. The claimed subject matter of claim 15

regarding input devices in input stage performing port expansion function and output

devices in output stage performing port contraction function is not adequately disclosed

in specification and consequently raises doubt to one of ordinary skill in the art of

Applicant's possession of invention at the time of filing. Examiner respectfully requests

Applicant to point out specific pages disclosing these limitations to resolve this issue.

Claim Rejections - 35 USC § 102

6.   The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that

form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

7.   Claims 1-10 are rejected under 35 U.S.C. 102(e) as being anticipated by Turner

et al.

Regarding claims 1, 6-8, 10,the claimed switch fabric comprising a switching

stage between an input stage and output stage is disclosed by multistage network, a

three-stage network (Figure 3). The claimed input stage is configured to receive the

data transmitted from one or more sources and forward data to switching stage is

disclosed by source buffers (Figure 3, elements 50, 52, 54, 56) send cells to first stage

switching elements (elements 60, 62, 64, 66). See column 5, lines 44-53, column 6,

lines 34-36. The claimed switching stage is configured to route data received from input

stage to the output stage is disclosed by cells being moved from buffers in input stage

to second stage (Figure 3, elements 70,72,74,76) and to third stage (elements

80,82,84,86) based on timestamps of cells. See column 6, lines 8-67, column 7, lines

1-44. The claimed output stage is configured to transmit data received from switching

stage towards one or more destinations is disclosed by third stage (Figure 3) outputs to

destinations where the multistage stage network routes a plurality of packets between a

plurality of sources and plurality of destinations via a plurality of paths passing through

these stages. See column 3, lines 31-44, column 6, lines 8-67, column 7, lines 1-44.

The claimed input and output stages have one or more input/output ports

respectively is disclosed by switching elements making up first stage (elements 60, 62,

64, 66) have four ports each and switching elements making up third stage (elements

80, 82, 84, 86) have four ports each. See column 5, lines 44-53.

The claimed input stage comprises a plurality of routing queues configured to

store data until data is ready to be forwarded to switching stage is disclosed by arrival

and departure buffers (elements 90, 92). See column 5, lines 44-53.

The claimed output stage comprises a plurality of routing queues configured to

store data until data is ready to be forwarded to one or more destinations is disclosed by

arrival and departure buffers in switching elements of each stage including output stage.

See column 5, lines 44-53 and Figure 3.

The claimed at least one input port can be programmably configured to store

data in two or more routing queues that are associated with a single output port is

disclosed by packets stored in arrival and departure buffers of the 4 switching elements

of input stage and these packets are routed in second and third stages to appropriate

departure buffers for outgoing link that lies on path to destination port specified in cell

header. See column 6, lines 8-67, column 7, lines 1-44.

The claimed at least one output port can be programmably configured to store

data in two or more routing queues that are associated with a single output port is

disclosed by packets originating from ports in switching elements of input stage where

packets are stored in arrival and departure buffers of the 4 switching elements of output

stage and routed on outgoing link that lies on path to destination port specified in cell

header. See column 6, lines 8-67, column 7, lines 1-44.

Regarding claims 2-4, the claimed different sets of data received at an input port

from a single source can be stored in two or more different input routing queues is

disclosed by assigning a packet passing through system a priority and by handling

packets with different priorities differently. Each buffer of switching elements (Figure 3)

is replaced by m buffers where each buffer contains packets of different priority class

(Figure 8, element 800). Each source port (elements 801-804) can supply packets to

any one of three buffers. See column 8, lines 41-64. The claimed different sets of data

to be transmitted at an output port to a single destination can be stored in two or more

different output routing queues is disclosed by switching element (element 800) is

contained in third stage of multistage network with priority departure buffers to

destinations. See column 8, lines 41-64.

Regarding claim 5, the data received at input port from single source can be

separated into different sets of data to achieve isolation is disclosed by traffic going to

congested outputs are isolated by maintaining separate buffers within switch elements

for the three stages. See column 10, lines 11-67, column 11, lines 1-43.

Regarding claim 9, the claimed maximum number of input/output routing queues

in each input device is a function of (1) the number of input/output ports and (2) the total

number of output/input ports in output/input stage is disclosed by 2 slot arrival buffer

and 4 slot departure buffer in switching elements of input stage and output stage.

Claim Rejections - 35 USC § 103

8.   The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all

obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set
> forth in section 102 of this title, if the differences between the subject matter sought to be patented and
> the prior art are such that the subject matter as a whole would have been obvious at the time the
> invention was made to a person having ordinary skill in the art to which said subject matter pertains.
> Patentability shall not be negatived by the manner in which the invention was made.

This application currently names joint inventors. In considering patentability of

the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of

the various claims was commonly owned at the time any inventions covered therein

were made absent any evidence to the contrary. Applicant is advised of the obligation

under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was

not commonly owned at the time a later invention was made in order for the examiner to

consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g)

prior art under 35 U.S.C. 103(a).

9.   Claims 11-25 are rejected under 35 U.S.C. 103(a) as being unpatentable over

Turner et al. (hereinafter referred to as Turner) in view of Angle et al. US 6,519,225

(hereinafter referred to as Angle).

Regarding claims 11-12, 15, 18, the claimed input stage, switching stage and

output stage is disclosed by three-stage network (Figure 3). Turner discloses all of the

limitations of the claims except for input stage transmits bids to switching stage to

request connections through switching stage for routing data, output stage transmits

status information about output stage to input stage and input stage is configured to

generated bids based on status information. Angle discloses transmit requests from

input ports (Figure 1, element 107) to fabric configuration manager (element 110). The

fabric configuration manager also receives control information from output ports

regarding traffic. See column 6, lines 40-55. The scheduler, as part of fabric

configuration manager, goes through grant and accept phases in order to determine

whether to grant transmission to input ports. See column 7, lines 39-67, column 8, lines

1-29.

At the time the invention was made it would have been obvious to a person of

ordinary skill in the art to modify Turner with grant/request scheme through stages of

Angle. One of ordinary skill in the art would be motivated to do so for efficient output

link scheduling. See column 1, lines 59-67, column 2, lines 1-9.

Regarding claims 13-14, 16-17, the claimed output stage comprising plurality of

output routing queues and plurality of output ports to store data prior to being

transmitted to one or more destinations is disclosed by packets originating from ports in

switching elements of input stage where packets are stored in arrival and departure

buffers of the 4 switching elements of output stage and routed on outgoing link that lies

on path to destination port specified in cell header. See column 6, lines 8-67, column 7,

lines 1-44.

Turner discloses all of the limitations of claim except for the claimed status

information about output stage that is used by input stage corresponds to per queue

status information and per port status information. Angle discloses backpressure signal

(Figure 2, element 250) from output ports to fabric configuration manager that identifies

output ports (element 109) having one or more output queues that have exceeded a

predetermined threshold of pending cells. As a result of backpressure, input ports do

not transfer cells to an output port that is back pressuring. See column 6, lines 52-67,

column 7, lines 1-12. At the time the invention was made it would have been obvious to

a person of ordinary skill in the art to modify Turner with backpressure signal of Angle.

One of ordinary skill in the art would be motivated to do so for efficient output link

scheduling. See column 1, lines 59-67, column 2, lines 1-9.

Regarding claims 19-20, 25, Turner discloses all of the limitations of the claims

except for input stage configured to transmit bids to switching stage to request

connections, switching stage to determine whether to accept or reject bid and to

transmit a grant/rejection signal to input stage identifying whether bid is accepted or

rejected, the signal identifying a reason for rejecting the bid and input stage determines

how to react based on reason. Angle discloses backpressure signal (Figure 2, element

250) from output ports to fabric configuration manager that identifies output ports

(element 109) having one or more output queues that have exceeded a predetermined

threshold of pending cells. As a result of backpressure, input ports do not transfer cells

to an output port that is back pressuring. See column 6, lines 52-67, column 7, lines 1-

12. At the time the invention was made it would have been obvious to a person of

ordinary skill in the art to modify Turner with backpressure signal of Angle. One of

ordinary skill in the art would be motivated to do so for efficient output link scheduling,

centralized backpressure handling and improved performance in terms of scheduling

delay and flexibility See column 1, lines 59-67, column 2, lines 1-9, column 7, lines 3-23.

Regarding claims 21-24, Turner discloses all of the limitations except for input device configured to transmit, if initial bid is rejected due to bid collision, a subsequent bid for the same output device and to transmit, if initial bid is rejected due to backpressure, a subsequent bid for a different output device. Angle discloses input port cycled through in circular fashion as part of arbitration for output port so if input port with higher priority gets request granted, next iteration will involve next input port that has a non granted request and it can request output port again and if output port is unavailable, other output ports are used. A reason for unavailability could be backpressure of output port. A backpressure signal (Figure 2, element 250) is sent from output ports to fabric configuration manager that identifies output ports (element 109) having one or more output queues that have exceeded a predetermined threshold of pending cells. As a result of backpressure, input ports do not transfer cells to an output port that is back pressuring. See column 6, lines 52-67, column 7, lines 1-12. See column 7, lines 39-67, column 8, lines 30-64. At the time the invention was made it would have been obvious to a person of ordinary skill in the art to modify Turner with backpressure signal of Angle. One of ordinary skill in the art would be motivated to do so for efficient output link scheduling, centralized backpressure handling and improved performance in terms of scheduling delay and flexibility See column 1, lines 59-67, column 2, lines 1-9, column 7, lines 3-23.

Conclusion

10.   The prior art made of record and not relied upon is considered pertinent to

applicant's disclosure.

- McKeown US 5,500,858 discloses scheduling cells in an input-queued switch.

- Isoyama et al. US 6,570,873 disclose scheduling reservation of traffic with

  priority.

- Koning et al. US 6,125,112 disclose non-buffered, non-blocking multistage ATM

  switch.

- Bauman et al. US 6,160,812 disclose supplying requests to a scheduler in an

  input buffered multiport switch.

- Cisneros US 5,157,654 discloses technique for resolving output port contention

  in high-speed switch.

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Melanie Jagannathan whose telephone number is 571-

272-3163. The examiner can normally be reached on Monday-Friday from 8:00 a.m.-

4:30 p.m..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Seema Rao can be reached on 571-272-3174. The fax phone number for

the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the

Patent Application Information Retrieval (PAIR) system. Status information for

published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

MJ ᴄ̃ᴏ̃
9/7/05

FRANK DUONG
PRIMARY EXAMINER